

Towards a Hierarchical Quantum Circuit Language

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Many circuit-based quantum programming languages contain limited functionality for handling programs with quantum branching events. In this paper we introduce a hierarchical circuit language with two new connectives, one for generalized controlled operations and one for sub-circuit exponentiation, to reason formally about such programs. We construct the hierarchical circuit language using a novel category-theoretic construction, demonstrate that its compositional properties are non-trivial, show a circuit identity which may be useful for circuit optimization by a compiler, demonstrate how generalized controlled operations describe quantum control flow, and then contrast generalized controlled operations with Pauli rotations.

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1 Introduction

As quantum computing hardware continues to advance, so too does its software. While quantum circuits remain the de facto standard language for low-level quantum programming, they are cumbersome when designing large-scale programs with tens of thousands of gates. Higher-level programming abstractions of quantum circuits are clearly on the horizon. In particular, quantum control flow [19] has proven to be challenging to implement since the control qubits in a quantum conditional statement may be in superposition, and can therefore become entangled with the data register [21]. A number of existing programming languages contain classical conditional statements [7, 18], and some allow features like uncomputation [4, 9] or coherent control [2]. Recently, Heunen et al. introduced a language with a quantum branching primitive for constructing controlled unitaries [11]. In this paper we introduce a new hierarchical circuit language which generalizes several existing notions of controlled sub-circuits (e.g. the `if-let` statement of [11]) and unifies this with the sub-circuit exponentiation found in OpenQASM 3 [7]. The language builds upon a novel category-theoretic construction. We expect that the circuit abstractions it provides will prove useful for control flow and higher-level compiler optimization.

In Sec. 2 we formally introduce the hierarchical circuit language, as a universal algebraic (see, [1]) extension of PROP categories (see, [3, 6, 13]). We describe the inductive construction that is used to generate the hierarchical language via closures over an increasing family of nested sub-languages, and then provide its semantics and some of its non-trivial compositional properties. Then we give a concrete example of a gate set for hierarchical circuits that is universal for quantum computation. In Sec. 3 we outline some properties of hierarchical circuits which may be useful for control flow or higher-level circuit optimization. We contrast this new abstraction with Pauli rotations, a common gate abstraction whose semantics also involve exponentiation.

2 Hierarchical Quantum Circuits

The hierarchical language introduced in this section builds upon the PROP formalism for quantum circuits. Recall that in the PROP formalism, a family of quantum circuits is defined with respect to a gate set \mathcal{G} . Each gate $G \in \mathcal{G}$ has an associated signature $G : n \rightarrow n$ which states that the gate G

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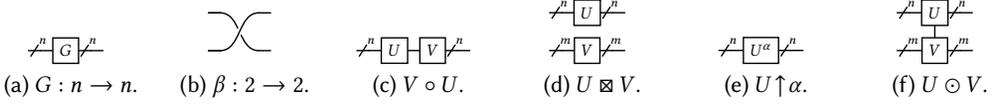


Fig. 1. The graphical language for hierarchical quantum circuits.

has n input wires and n output wires. Gates can be composed sequentially and in parallel, using the sequential composition operator (\circ) and the parallel composition operator (\boxtimes). Given $n \in \mathbb{N}$, we write $\text{Prop}(\mathcal{G})(n)$ for the collection of all circuits expressible over \mathcal{G} using the empty wire which is denoted 1 , the wire crossing (β), sequential composition, and parallel composition. Formally, $\text{Prop}(\mathcal{G})$ is the free PROP category generated by \mathcal{G} which corresponds to the least fixed-point of the following inductive definition where (\circ) and (\boxtimes) are viewed as syntactic connectives.

- $1 \in \text{Prop}(\mathcal{G})(1)$ and $\beta \in \text{Prop}(\mathcal{G})(2)$.
- If $G \in \mathcal{G}$ and $G : n \rightarrow n$ then $G \in \text{Prop}(\mathcal{G})(n)$.
- If $U \in \text{Prop}(\mathcal{G})(n)$ and $V \in \text{Prop}(\mathcal{G})(n)$, then $V \circ U \in \text{Prop}(\mathcal{G})(n)$.
- If $U \in \text{Prop}(\mathcal{G})(n)$ and $V \in \text{Prop}(\mathcal{G})(m)$ then $U \boxtimes V \in \text{Prop}(\mathcal{G})(n+m)$.

The hierarchical circuit language introduces two new connectives, (\uparrow) and (\odot), which correspond to sub-circuit exponentiation and generalized controls, respectively. The (\uparrow) connective takes a circuit $U : n \rightarrow n$ together with a real number $\alpha \in \mathbb{R}$, and then return a circuit $(U \uparrow \alpha) : n \rightarrow n$ with the intended interpretation that U is exponentiated by α . The (\odot) connective takes two circuits $U : n \rightarrow n$ and $V : m \rightarrow m$, and then return a new circuit $U \odot V : (n+m) \rightarrow (n+m)$ with the intended interpretation that V is controlled by U . The graphical language is depicted in Fig. 1.

Each circuit in the hierarchical language has an associated level, which captures the nesting depth of these new connectives. The level of a circuit is important, both from the point-of-view of compilation and relational theories. To encode the notion of levels, the hierarchical circuit language is defined inductively, starting from a primitive gate set \mathcal{G} and the language of level zero circuits, denoted $\mathcal{C}_0 = \text{Prop}(\mathcal{G})$. The first step is to introduce powers of each circuit. Formally, for each collection of circuits \mathcal{C} define the set $\text{Exp}(\mathcal{C})$ of circuit exponentials as follows.

$$\text{Exp}(\mathcal{C}) = \{(U \uparrow \alpha) : n \rightarrow n \mid U \in \mathcal{C}(n) \text{ and } \alpha \in \mathbb{R}\}$$

Then $\mathcal{G}_0^{exp} = \text{Exp}(\mathcal{G}) \cup \mathcal{G}$ and $\mathcal{C}_0^{exp} = \text{Prop}(\mathcal{G}_0^{exp})$. The second step is to close the gate set under generalized controls. Formally, $\text{Ctrl}(\mathcal{C})$ is the smallest gate set such that if U is in $(\mathcal{C} \cup \text{Ctrl}(\mathcal{C}))(n)$ and V is in $(\mathcal{C} \cup \text{Ctrl}(\mathcal{C}))(m)$, then $U \odot V$ is in $\text{Ctrl}(\mathcal{C})(n+m)$. Then $\mathcal{G}_1 = \text{Ctrl}(\mathcal{G}_0^{exp}) \cup \mathcal{G}_0^{exp}$ and $\mathcal{C}_1 = \text{Prop}(\mathcal{G}_1)$. The construction then proceeds as follows for $n \geq 1$.

- $\mathcal{G}_n^{exp} = \text{Exp}(\mathcal{C}_n) \cup \mathcal{G}_n$ and $\mathcal{C}_n^{exp} = \text{Prop}(\mathcal{G}_n^{exp})$.
- $\mathcal{G}_{n+1} = \text{Ctrl}(\mathcal{G}_n^{exp}) \cup \mathcal{G}_n^{exp}$ and $\mathcal{C}_{n+1} = \text{Prop}(\mathcal{G}_{n+1})$.

This gives rise to the following inclusion of sub-languages.

$$\begin{array}{ccccccccccc} \mathcal{G} & \hookrightarrow & \mathcal{G}_0^{exp} & \hookrightarrow & \mathcal{G}_1 & \hookrightarrow & \mathcal{G}_1^{exp} & \hookrightarrow & \mathcal{G}_2 & \hookrightarrow & \mathcal{G}_2^{exp} & \hookrightarrow & \mathcal{G}_3 & \hookrightarrow & \dots \\ \downarrow & & \\ \mathcal{C}_0 & \hookrightarrow & \mathcal{C}_0^{exp} & \hookrightarrow & \mathcal{C}_1 & \hookrightarrow & \mathcal{C}_1^{exp} & \hookrightarrow & \mathcal{C}_2 & \hookrightarrow & \mathcal{C}_2^{exp} & \hookrightarrow & \mathcal{C}_3 & \hookrightarrow & \dots \end{array}$$

The *hierarchical circuit language* obtained from \mathcal{G} is then defined to be $\text{LvProp}(\mathcal{G}) = \bigcup_{n=1}^{\infty} \mathcal{C}_n$. Given a circuit $U \in \text{LvProp}(\mathcal{G}_0)(n)$, the *level* U denoted $\text{lv}(U)$ is defined to be the least integer ℓ such that $U \in \mathcal{C}_\ell(n)$. If $\text{lv}(U) = \ell$, then U contains a gate built from ℓ layers of nested circuits.

The matrix semantics for a circuit language $\mathcal{C} = \text{Prop}(\mathcal{G})$ are defined by a mapping F from \mathcal{G} to unitary matrices such that if $G : n \rightarrow n$ then $F(G)$ is a $(2^n) \times (2^n)$ matrix. The mapping F then extends inductively to a function from circuits in \mathcal{C} to matrices as follows.

- If $G \in \mathcal{G}$, then $\llbracket G \rrbracket = F(G)$.

- If $U \in C(n)$ and $V \in C(n)$, then $\llbracket V \circ U \rrbracket = \llbracket V \rrbracket \llbracket U \rrbracket$.
- If $U \in C(n)$ and $V \in C(m)$, then $\llbracket U \boxtimes V \rrbracket = \llbracket U \rrbracket \otimes \llbracket V \rrbracket$.

The remainder of this section will explore how $\llbracket - \rrbracket$ extends to $\text{LvProp}(\mathcal{G})$.

2.1 The semantics of sub-circuit exponentiation

Recall that $\exp(M) := \sum_{n=0}^{\infty} \frac{M^n}{n!}$. For each unitary matrix U , there exists a unique skew-Hermitian matrix M with spectrum in $i(-\pi, \pi]$ such that $\exp(M) = U$. This defines a branch cut $\text{Log}(-)$ of $\exp^{-1}(-)$ when restricted to unitary matrices. If U is a unitary matrix, and $\alpha \in \mathbb{R}$, then U^α is defined to be $\exp(\text{Log}(U)\alpha)$ as in [7]. Consequently, $\llbracket U \uparrow \alpha \rrbracket := \llbracket U \rrbracket^\alpha = \exp(\text{Log}(U)\alpha)$.

Unfortunately, (\uparrow) is not an action by (\mathbb{R}, \cdot) with respect to the semantics of $\llbracket - \rrbracket$. This means that there exists a $U \in C(n)$ and $\alpha, \beta \in \mathbb{R}$ such that $\llbracket (U \uparrow \alpha) \uparrow \beta \rrbracket \neq \llbracket U \uparrow (\alpha\beta) \rrbracket$. In terms of compilation, this means that rewriting a term of the form $(U \uparrow \alpha) \uparrow \beta$ to $U \uparrow (\alpha\beta)$ is not a sound optimization. For example, consider the case where $\llbracket U \rrbracket = Z$. Since Z is diagonal, then the following equations hold.

$$M = \text{Log}\llbracket U \rrbracket = \begin{bmatrix} \text{Log}(1) & 0 \\ 0 & \text{Log}(-1) \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ 0 & i\pi \end{bmatrix} \quad \llbracket U \uparrow \alpha \rrbracket = \exp(M\alpha) = \begin{bmatrix} 1 & 0 \\ 0 & e^{i\alpha\pi} \end{bmatrix}$$

It can then be asked whether $\llbracket (U \uparrow \alpha) \uparrow \beta \rrbracket = \llbracket U \uparrow (\alpha\beta) \rrbracket$. This requires solving for $\text{Log}\llbracket U \uparrow \alpha \rrbracket$.

- If $\alpha \in (-1, 1]$, then $\alpha\pi \in (-\pi, \pi]$, and consequently $\text{Log}\llbracket U \uparrow \alpha \rrbracket = \alpha M$. It then follows by definition that $\llbracket (U \uparrow \alpha) \uparrow \beta \rrbracket = \exp(M(\alpha\beta)) = \llbracket U \uparrow (\alpha\beta) \rrbracket$.
- If $\alpha \notin (-1, 1]$, then $\text{Log}\llbracket U \uparrow \alpha \rrbracket = \bar{\alpha}M$ where $\bar{\alpha}$ is the remainder of α modulo π . Then $\llbracket (U \uparrow \alpha) \uparrow \beta \rrbracket = \llbracket U \uparrow (\alpha\beta) \rrbracket$ if and only if $\exp(M(\bar{\alpha}\beta)) = \exp(M(\alpha\beta))$, which means that $\bar{\alpha}\beta$ and $\alpha\beta$ differ by an integer multiple of 2π . It follows by a short calculation that this happens if and only if β is a rational number whose denominator divides $(\alpha - \bar{\alpha})$.

This generalizes to diagonal matrices with one or more eigenvalues λ outside $\{0, 1\}$, by replacing α with $\lambda\alpha$ in each step of the argument. The argument then generalizes to non-diagonal Hermitian matrices M by first diagonalizing M as $P^\dagger DP$, and then using the fact that $\exp(iM\alpha) = P^\dagger \exp(iD\alpha)P$. In Theorem 2.1, the most general case is handled. Since the restriction to $(-1, 1]$ does define a monoid action, then exponentiation can be used to define the roots of circuits and their inverses.

THEOREM 2.1. *Let $U \in C(n)$ with $\{\lambda_j\}_{j=1}^{2^n}$ the eigenvalues of $\text{Log}(\llbracket U \rrbracket)$. For each $\alpha \in \mathbb{R}$ and $\beta \in \mathbb{R}$, $\llbracket (U \uparrow \alpha) \uparrow \beta \rrbracket = \llbracket U \uparrow (\alpha\beta) \rrbracket$ if and only if for each $j \in [2^n]$ either $\lambda_j\alpha \in i(-\pi, \pi]$ or $\beta \in \mathbb{Q}$ with reduced denominator dividing $\lfloor \lambda_j\alpha / (2i\pi) \rfloor$. This means that $(-1, 1]$ is the largest sub-monoid of (\mathbb{R}, \cdot) such that (\uparrow) restricts to an action with respect to the semantics of $\llbracket - \rrbracket$.*

2.2 The semantics of generalized controls

Let $U \in C(n)$ and $V \in C(m)$. Then define $\llbracket U \circ V \rrbracket := \exp(M_U \otimes M_V / (i\pi))$ where $M_U = \text{Log}\llbracket U \rrbracket$ and $M_V = \text{Log}\llbracket V \rrbracket$. Since the spectra of M_U and M_V are in $i(-\pi, \pi]$, then the spectrum of $M_U \otimes M_V / (i\pi)$ is in $i(-\pi, \pi]$. This means that the \odot -product is associative with respect to $\llbracket - \rrbracket$. An n -fold \odot -product therefore takes the form $\llbracket \bigotimes_{j=1}^n U_j \rrbracket = \exp\left(\left(\bigotimes_{j=1}^n \text{Log}\llbracket U_j \rrbracket\right) / (i\pi)^{n-1}\right) = \exp\left(i\pi \bigotimes_{j=1}^n \frac{\text{Log}\llbracket U_j \rrbracket}{i\pi}\right)$.

If there exists some circuit $E \in C(0)$ such that $\llbracket E \rrbracket = -1$, then the \odot -product is also unital and can be related to sub-circuit exponentiation. Since $\text{Log}(\llbracket E \rrbracket) = \text{Log}(-1) = \text{Log}(e^{i\pi}) = i\pi$, then $\llbracket E \circ U \rrbracket = \llbracket U \rrbracket = \llbracket U \circ E \rrbracket$ for each circuit U in C . Moreover, if $\alpha \in (-1, 1]$, then $\text{Log}(\llbracket E \uparrow \alpha \rrbracket) = \alpha\pi$. It follows that $\llbracket (E \uparrow \alpha) \circ U \rrbracket = \llbracket U \uparrow \alpha \rrbracket$ on the domain where (\uparrow) is a monoid action.

2.3 A concrete example

A surprising result is that the following hierarchical gate set is universal for quantum computation.

$$\mathcal{G} = \left\{ \bullet, \text{---}, \text{---}\oplus\text{---} \right\} \quad \text{where} \quad \llbracket \bullet \rrbracket = -1 \quad \llbracket \text{---} \rrbracket = Z \quad \llbracket \text{---}\oplus\text{---} \rrbracket = X.$$

the normal form of $U \odot V$ will have a term “if let $|u_j \otimes v_k\rangle$ then $\text{Ph}(\pi\theta_j\rho_k)$,” for each $j \in [2^n]$ and $k \in [2^m]$. This means (\odot) is a primitive that describes quantum control flow by packaging together branching statements over the eigenbases of U and V .

3.2 Simulating Pauli rotations with hierarchical circuits

Pauli rotations [10, 12], sometimes called Pauli gadgets [17], are defined as $R_P(\theta) = \exp(-i\theta P/2)$ where $\theta \in \mathbb{R}$ and $P = \bigotimes_{j=1}^n P_j$ is an n -fold tensor product of Pauli matrices $P_j \in \{\pm I, \pm X, \pm Y, \pm Z\}$. Pauli rotations are widely used in circuit optimization [20], benchmarking [14], and chemistry applications [17].

Pauli rotations bear some semblance to the (\odot) and (\uparrow) connectives of hierarchical circuits, since all three notions have semantics of the form $\exp(-)$. In particular, if $P(n) = Z^{\otimes n}$, then it may appear possible that $R_{P(n)}(\theta) = \llbracket G(n, \theta) \rrbracket$ for some *control tower* $G(n, \theta) := (U^{\otimes n}) \uparrow \phi(\theta)$ composed of n stacked copies of a fixed diagonal one-qubit circuit U , raised to an overall power $\phi(\theta)$ which encodes the rotation angle θ . However, no such $G(n, \theta)$ exists, as the sub-circuit U would need to satisfy $\text{Log}\llbracket U \rrbracket = i\pi Z$, which violates the assumption that $\text{Log}\llbracket U \rrbracket$ has eigenvalues in $i(-\pi, \pi]$. Nor does there exist a choice of k towers $G_1(n, \theta)$ through to $G_k(n, \theta)$ such that the circuit $U = G_k(n, \theta) \circ \dots \circ G_1(n, \theta)$ of constant depth k satisfies $\llbracket U \rrbracket = R_{P(n)}(\theta)$ for all n and θ . To simulate a Pauli rotation using a circuit of control towers, the circuit depth k must depend on n . This is also the case when simulating a Pauli rotation using the Clifford+ T gate set; each Pauli rotation must be compiled into a subcircuit whose depth depends on n .

However, the expressiveness of hierarchical circuits does allow for a Pauli rotation to be expressed as a single *non-uniform* control tower $G_f(n, \theta)$ where U_n and ϕ_n are allowed to depend on n via a function $f : \mathbb{N} \rightarrow (1, \infty)$, and $\text{lv}(U_n) = 1$. The function f serves to rescale the eigenvalues of $\text{Log}\llbracket U_n \rrbracket$ to lie in $i(-\pi, \pi]$, which can then be corrected for in ϕ_n .

$$\begin{array}{ccc}
 \begin{array}{c} \boxed{R_{P(n)}(\theta)} \\ \text{(no uniform } G \text{ exists } \forall n) \end{array} & \neq & \begin{array}{c} \begin{array}{c} \boxed{U} \\ \vdots \\ \boxed{U} \end{array} \uparrow \phi(\theta) \end{array} \\
 & & \text{however} \\
 \begin{array}{c} \boxed{R_{P(n)}(\theta)} \\ \text{(non-uniform solution } G_f \forall n) \end{array} & = & \begin{array}{c} \begin{array}{c} \boxed{U_n} \\ \vdots \\ \boxed{U_n} \end{array} \uparrow \phi_n(\theta) \end{array}
 \end{array}
 \quad \text{where} \quad
 \begin{array}{l}
 U_n := \text{---} \oplus \text{---} \overset{\frac{1}{f(n)}}{\bullet} \text{---} \oplus \text{---} \overset{-1}{\bullet} \text{---} \\
 \phi_n(\theta) := -(f(n))^n \theta / (2\pi) \\
 \llbracket U_n \rrbracket := \exp(i\pi Z / f(n))
 \end{array}$$

Two interesting cases are when $f(n)$ is constant and when $f(n)$ is the n -th root of a constant. For example, if $f(n) = 2$, then $\phi_n(\theta) = 2^n \theta / (2\pi)$ depends explicitly on the value of n , but $\llbracket U_n \rrbracket = iZ$ does not. In contrast, if $f(n) = \sqrt[n]{2}$, then $\llbracket U_n \rrbracket = \exp(i\pi Z / \sqrt[n]{2}) = \cos(\pi / \sqrt[n]{2})I + i \sin(\pi / \sqrt[n]{2})Z$ depends explicitly on the value of n , but $\phi_n(\theta) = -\theta/\pi$ does not.

4 Future Work

In this paper, we introduced a hierarchical quantum circuit language for reasoning about controls and exponentiation. It was shown that generalized controls are associative, whereas sub-circuit exponentiation is only a monoid action under special assumptions. A component-wise diagonalization was then introduced which generalizes that found in [8, 9], and is shared by Pauli rotations. This property also leads to a normal form which can be expressed in terms of the normal terms found in [11], showing that generalized controls are indeed an abstraction of quantum control flow. It was then shown how these new primitives differ from Pauli rotations, and how Pauli rotations can be compiled to $O(1)$ -depth hierarchical circuits, provided that a non-uniform decomposition is allowed. In future work, we would like to explore the interaction of generalized controls with sub-circuit exponentiation, in the hopes of obtaining an equational theory which generalizes [8]. We are also interested to extend hierarchical circuits to non-unitary circuits involving measurement, initialization, and discarding as in [2, 5].

5 Proof that no constant-depth circuit of uniform control towers can simulate a Pauli rotation for all n

Let U_j be an arbitrary diagonal one-qubit circuit so that $\llbracket U_j \rrbracket = \text{diag}(e^{i\pi\lambda_{j,0}}, e^{i\pi\lambda_{j,1}})$ and $\text{Log}\llbracket U_j \rrbracket / (i\pi) = \text{diag}(\lambda_{j,0}, \lambda_{j,1})$ with $\lambda_{j,0}, \lambda_{j,1} \in (-1, 1]$. Let $G_j(n, \theta) := \left(U_j^{\otimes n} \right) \uparrow \phi_j(\theta)$ be the *uniform* control tower constructed from n stacked copies of U_j , with a power given by a real function $\phi_j(\theta)$, as above. Note that neither U_j nor ϕ_j can depend on n , since G_j is uniform. Consider the circuit $U = G_k \circ \dots \circ G_1$ comprised of k control towers G_j composed in sequence, with $j \in [k]$, and $k \in \mathbb{N}$. The depth of U is the constant k .

THEOREM 5.1. *There does not exist a constant depth k circuit U comprised of uniform control towers G_j that satisfies $\llbracket U \rrbracket = R_{P(n)}(\theta)$ for all n and θ .*

PROOF. Assume there exists a U satisfying $\llbracket U \rrbracket = R_{P(n)}(\theta)$ for all n . U has semantic interpretation

$$\llbracket U \rrbracket = \llbracket G_k \circ \dots \circ G_1 \rrbracket = \prod_{j=1}^k \llbracket G_j \rrbracket = \prod_{j=1}^k \left(\left(U_j^{\otimes n} \right) \uparrow \phi_j(\theta) \right) \quad (1)$$

$$= \prod_{j=1}^k \exp(\phi_j(\theta) \text{Log}(\exp(i\pi \bigotimes_{l=1}^n \frac{\text{Log}\llbracket U_j \rrbracket}{i\pi}))) \quad (2)$$

$$= \prod_{j=1}^k \exp(i\pi \phi_j(\theta) \bigotimes_{l=1}^n \frac{\text{Log}\llbracket U_j \rrbracket}{i\pi}) \quad (3)$$

Since U_j is diagonal for all j , it follows that $\bigotimes_{l=1}^n \text{Log}\llbracket U_j \rrbracket / i\pi$ is also diagonal for all j . Therefore $\bigotimes_{l=1}^n \text{Log}\llbracket U_j \rrbracket / i\pi$ commutes with $\bigotimes_{l=1}^n \text{Log}\llbracket U_{j'} \rrbracket / i\pi$ for all j, j' , and the Baker-Campbell-Hausdorff identity gives us

$$= \exp(i\pi \sum_{j=1}^k \phi_j(\theta) \bigotimes_{l=1}^n \frac{\text{Log}\llbracket U_j \rrbracket}{i\pi}) \quad (4)$$

Now let $\frac{\text{Log}\llbracket U_j \rrbracket}{i\pi} =: \begin{bmatrix} \lambda_{j,0} & 0 \\ 0 & \lambda_{j,1} \end{bmatrix} = \lambda_{j,0} |0\rangle \langle 0| + \lambda_{j,1} |1\rangle \langle 1|$.

$$= \exp(i\pi \sum_{j=1}^k \phi_j(\theta) \sum_{x \in \{0,1\}^n} \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|} |x\rangle \langle x|) \quad (5)$$

$$= \exp\left(\sum_{x \in \{0,1\}^n} i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|} |x\rangle \langle x| \right) \quad (6)$$

$$= \sum_{x \in \{0,1\}^n} \left(e^{i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|}} \right) |x\rangle \langle x| \quad (7)$$

A Pauli rotation has the form

$$R_{P(n)}(\theta) = \exp\left(\frac{-i\theta}{2} \bigotimes_{l=1}^n Z\right) = \exp\left(\sum_{x \in \{0,1\}^n} \frac{-i\theta}{2} (-1)^{|x|} |x\rangle \langle x|\right) \quad (8)$$

$$= \sum_{x \in \{0,1\}^n} \left(e^{\frac{-i\theta}{2} (-1)^{|x|}} \right) |x\rangle \langle x| \quad (9)$$

Since $\llbracket U \rrbracket = R_{P(n)}(\theta)$, and both matrices are now written in an explicitly diagonal form, they must be equal in every entry x . Now consider $n \rightarrow \infty$. Since $\lambda_{j,0}$ and $\lambda_{j,1}$ were defined via the principal logarithm, we have $\lambda_{j,0}, \lambda_{j,1} \in (-1, 1]$. If $\lambda_{j,0} = \lambda_{j,1} = 1$ then $\llbracket G_j \rrbracket = e^{i\pi\phi_j(\theta)}I$ is just a global phase gate, and we pick another $j' \neq j$ that isn't a global phase gate. Note that there must be at least one G_j that isn't proportional to I , since if $G_j \propto I$ for all j , then $U \propto I$, which violates the assumption that $\llbracket U \rrbracket = R_{P(n)}(\theta)$. At least one of $\lambda_{j,0}, \lambda_{j,1} \neq 1$, let's say $\lambda_{j,1} \neq 1$ without loss of generality.

Consider the bottom-right entry where x is the all-1 string. Then $|x| = n$, and so $i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|} \rightarrow 0$ as $n \rightarrow \infty$. But then

$$e^{i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|}} = e^0 \neq e^{-\frac{i\theta}{2}(-1)^{|x|}} \quad (10)$$

unless $\theta = 0$. But then $\llbracket U \rrbracket$ cannot be equal to $R_{P(n)}(\theta)$ for all θ and n since they differ in this entry when $n \rightarrow \infty$ and $\theta \neq 0$, a contradiction. If instead $\lambda_{j,0} \neq 1$, then consider the top-left entry where x is the all-0 string. Then $|x| = 0$, and so $i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|} \rightarrow 0$ as $n \rightarrow \infty$, and again

$$\llbracket U \rrbracket = e^{i\pi \sum_{j=1}^k \phi_j(\theta) \lambda_{j,0}^{n-|x|} \lambda_{j,1}^{|x|}} = e^0 \neq e^{-\frac{i\theta}{2}(-1)^{|x|}} = R_{P(n)}(\theta) \quad (11)$$

unless $\theta = 0$, again a contradiction. In all cases we have $\llbracket U \rrbracket \neq R_{P(n)}(\theta)$ for at least some choices of n, θ , contradicting our premise. \square

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